What Is Claimed Is:

1	1. <i>i</i>	A first fiber	array hal:	f for retaining	optical fibers	, comprising:
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- a first chip having at least one groove formed in a top surface and extending
- 3 longitudinally from a front face toward a back face, said chip having a bottom, said groove(s)
- 4 being configured for receiving an individual optical fiber, respectively;
 - a first molded mount consisting of polymer material, said mount having top, and bottom portions, an open channel being formed through said top portion, said channel being configured to receive said chip for mounting therein; and

said chip being rigidly secured within said channel of said mount, wherein said channel is configured to provide exposure of the front face and top of said chip.

- 2. The fiber array of Claim 1, wherein the channel of said mount is longer than said chip, thereby providing a recessed area behind said chip.
- 1 3. The fiber array of Claim 1, wherein said chip is adhesively secured to said mount.
- 4. The fiber array of Claim 1, wherein said mount is molded to said chip.
- 5. The fiber array of Claim 1, wherein said chip and said mount are configured for providing
- 2 a press fit and fictional securement therebetween.

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other.

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- 1 11. The fiber array of Claim 10, further comprising adhesive for rigidly securing together the
- 2 first array half to the second array half.
- 1 12. The fiber array of Claim 10, further including:
- 2 means for locking said first and second mounts with first and second chips,
- 3 respectively, together in a manner causing respective grooves to apply a compressive force
- 4 against associated optical fibers retainer therebetween.
 - 13. The fiber array of Claim 12, wherein said means for locking includes:
 - a locking pawl protruding away from the top portion of like ends of each the of said first and second mounts, respectively; and
 - a keyway formed into the top portion of opposite like ends of each one of said first and second mounts, said locking pawls and keyways being configured to interlock with one-another for retaining said first and second mounts securely together.
 - 14. The fiber array of Claim 12, wherein said means for locking includes:
- said first mount configured to have first and second locking pawls protruding
 away from opposing ends of the top face thereof; and
- said second mount includes first and second L-shaped notches cut into its right and left side portions thereof, respectively;
- said first and second locking pawls of said first mount being configured for locking into said first and second L-shaped notches of said second mount, respectively, for

- 8 retaining said first and second mounts securely together.
- 1 15. The fiber array of Claim 12, wherein said locking means includes an ultrasonically
- welded joint in the polymer material.
- 16. The fiber array of Claim 1, further including:
 - said first silicon chip having a transverse slotway in a bottom face; and said first mount being formed with a transverse stepped portion protruding away from a bottom of said channel, for mating with said transverse slotway of said first silicon chip.
 - 17. The fiber array of Claim 1, further including:

vertical grooves formed at opposite ends of the front portion of said first mount, for providing control over the wicking of glue, and enhancing the bonding of glue.

- 18. The fiber array of Claim 1, wherein the front face of said first silicon chip extends from
- the front portion of said first molded mount for up to 100 micrometers.
- 1 19. The fiber array of Claim 1, further including:
- a strain relief extending from said back portion of said first molded mount.
- 1 20. The fiber array of Claim 1, further including:
- an open notch formed in said top portion of said first molded mount from said channel

- through one side portion thereof, for receiving strengthening fibers of an associated optical 3 fiber cable. 4 1 21. The fiber array of Claim 20, wherein said securement means consists of glue. 22. A fiber array for retaining optical fibers, comprising: 1 2 a first and second chips each having at least one groove formed in a top 3 surface and extending longitudinally from a front face toward a back face, said chip having a 4 grangs mile and the second of the second o bottom, said groove(s) being configured for receiving an individual optical fiber, respectively; and first and second molded mounts each consisting of polymer material, said mounts each having top, and bottom portions, an open channel being formed through said top portion said channel being configured to receive said first chip for mounting therein; said first chip being rigidly secured within said channel of said first molded mount, wherein said channel is configured to provide exposure of the front face and top of 11 first said chip, thereby providing a first fiber array half; 12 said second chip being rigidly secured within the channel of said second 13 molded mount, wherein the channel is configured to provide exposure of the front face and
 - said first fiber array half and said second fiber array half being disposed with respective grooves of one facing respective grooves of the other.

top of said second chip, thereby providing a second fiber array half;

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23. A fiber array for retaining optical fibers, comprising:

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first and second chips each having at least one groove formed in a top surface and extending longitudinally from a front face toward a back face, said chips having a bottom, said groove(s) being configured for receiving an individual optical fiber, respectively;

first and second molded mounts each consisting of polymer material, said mounts each having top, and bottom portions, an open channel being formed through said top portions said channels being configured to receive said first and second chips for mounting therein, respectively;

said first chip being rigidly secured within said channel of said first molded mount, wherein said channel is configured to provide exposure of the front face and top of first said chip;

said second silicon chip being rigidly secured in the channel of said second molded mount, wherein said channel is configured to provide exposure of the front face and top of said second silicon chip; and

means for locking said first and second molded mounts with first and second chips, respectively, together in a manner causing respective opposing grooves to apply a compressive force against associated optical fibers retained therebetween.